

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Egerer

Docket No.:

54382-20048.00

Serial No.:

10/812,395

Art Unit:

2836

Filed:

March 30, 2004

Examiner:

Rutland Wallis, Michael

For:

Semiconductor Device Voltage Supply for a System with at Least Two,

Especially Stacked, Semiconductor Devices

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Applicant wishes to bring to the attention of the Patent and Trademark Office the information noted on the enclosed form PTO/SB/08A that may be considered material to the examination of the above-identified application. Copies of the U.S. Patents cited are not being submitted. However, Applicant has included copies of the foreign patents.

In the Office Action dated December 15, 2006, the Examiner objected to the Information Disclosure Statement filed on March 30, 2004 because it did not include an explanation of the relevance of each of the listed patents that are not in the English language. The Applicant again submits the two previously listed German language patents on the attached Form PTO/SB/08a. These German language patents are relevant because they were cited in the parent German application.

Applicant does not have a full translation of the German patents, but has included a translation of the Abstracts. Applicant respectfully requests that the Examiner initial the attached Form PTO/SB/08a.

Respectfully submitted,

May 14, 2007

Date

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Complete if Known

PTO/SB/08a (09-06)
Approved for use through 03/31/2007. OMB 0651-0031
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ubotituto for form 1440A/DTO

Substi	tute for form 1449APTO .			Application Number	10/812,395
INI	EODMATION DISC	100	HIDE	Filing Date	March 30, 2004
INFORMATION DISCLOSURE				First Named Inventor	Egerer
S	STATEMENT BY APPLICANT			Art Unit	2836
	(Use as many sheets as nece	essary)		Examiner Name	Rutland Wallis, Michael
Sheet	1	of	1	Attorney Docket Number	54382-20048.00
			U.S. PATENT	DOCUMENTS	
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U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
	No. ¹	Number - Kind Code ^{2 (if known)}				
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FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No.1	Foreign Patent Document Country Code ³ - Number ⁴ - Kind Code ⁵ (<i>if known</i>)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	Τ°
	1	DE 19648492 A1	11-13-97	Siemens AG		
	2	DE 3806951 C2	09-15-88	SGS-Thomson Microelectronics S.P.A.		

Examiner	Date	
Signature	Considered	

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹Applicant's unique citation designation number (optional). ²See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. 5Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing; and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.